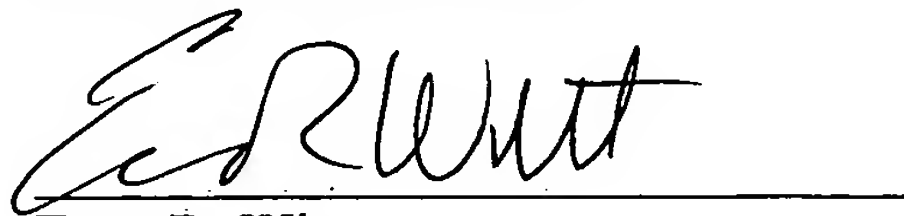


REMARKS/ARGUMENTS

By this Amendment, claims 1, 10, and 17 are amended and claims 9 and 16 are cancelled. Claims 1, 10, and 17 are amended to recite a microprocessor. Support for these amendments may be found in cancelled claims 9 and 16. The claims were further amended to recite that the second tile converter or the second portion of the tile converter is disposed between said core logic unit and said system memory. Support for this amendment may be found in Figs. 3 and 4.

Applicant expresses appreciation to Examiner Tung for discussing this application with Applicant's attorney on November 9, 2005. During that telephonic interview, the Advisory Action dated October 28, 2005 was discussed. Specifically, it was noted that the claims recite that the second tile converter is "in communication" with the core logic unit, whereas it was argued in Applicant's response to the final Office Action that the second tile converter was downstream of the core logic unit. Examiner Tung suggested that the claims be amended to positively recite that the second tile converter is disposed between the core logic unit and the system memory. It was agreed that this amendment distinguished the cited Surti et al. patent. The claims have been amended according to the Examiner's suggestion. Applicant respectfully submits that the pending claims are patentably distinct from the cited prior art and in condition for allowance. If there are any remaining issues preventing allowance of the pending claims that may be clarified by telephone, the Examiner is requested to call the undersigned.

Respectfully submitted,



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